

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,135,767 B2  
APPLICATION NO. : 10/629271  
DATED : November 14, 2006  
INVENTOR(S) : Wong et al.

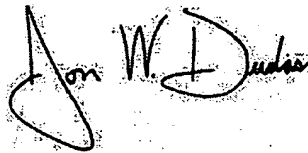
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 6, line 6, in Claim 7, after "semiconductor" insert -- substrate --.

Signed and Sealed this

Twentieth Day of March, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a faint, circular embossed seal of the United States Patent and Trademark Office.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*